



```

Device utilization summary:
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Selected Device : 3s500efg320-4

Number of Slices:                83 out of 4656 1%
Number of Slice Flip Flops:      134 out of 9312 1%
Number of 4 input LUTs:          111 out of 9312 1%
Number of IOs:                   6
Number of bonded IOBs:           6 out of 232 2%
Number of GCLKs:                  3 out of 24 12%

```

图 4 Sinc<sup>3</sup>滤波器综合报告

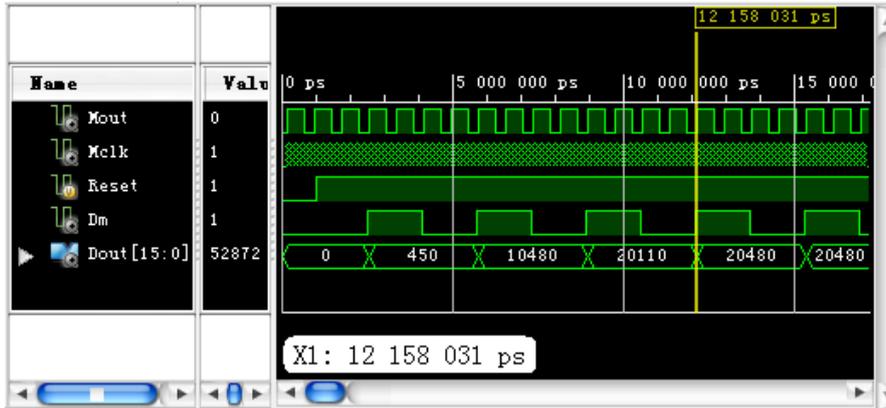


图 5 Sinc<sup>3</sup>滤波器后仿真

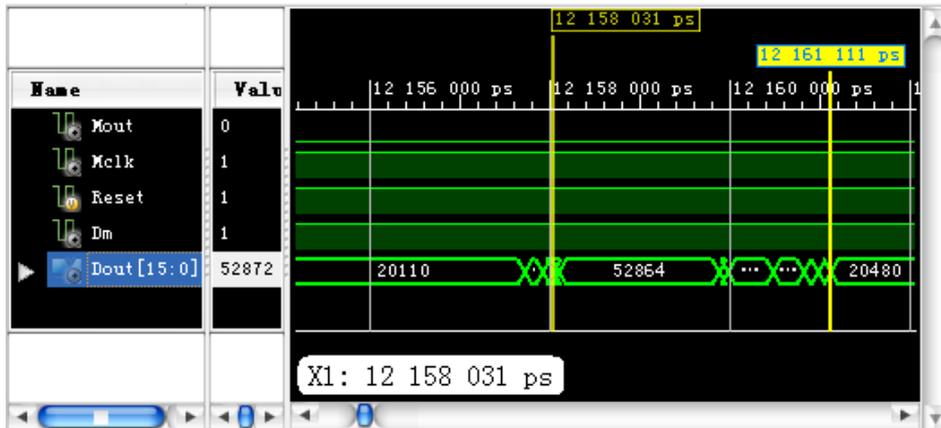


图 6 Sinc<sup>3</sup>滤波器后仿真局部放大图